

FIG. 1

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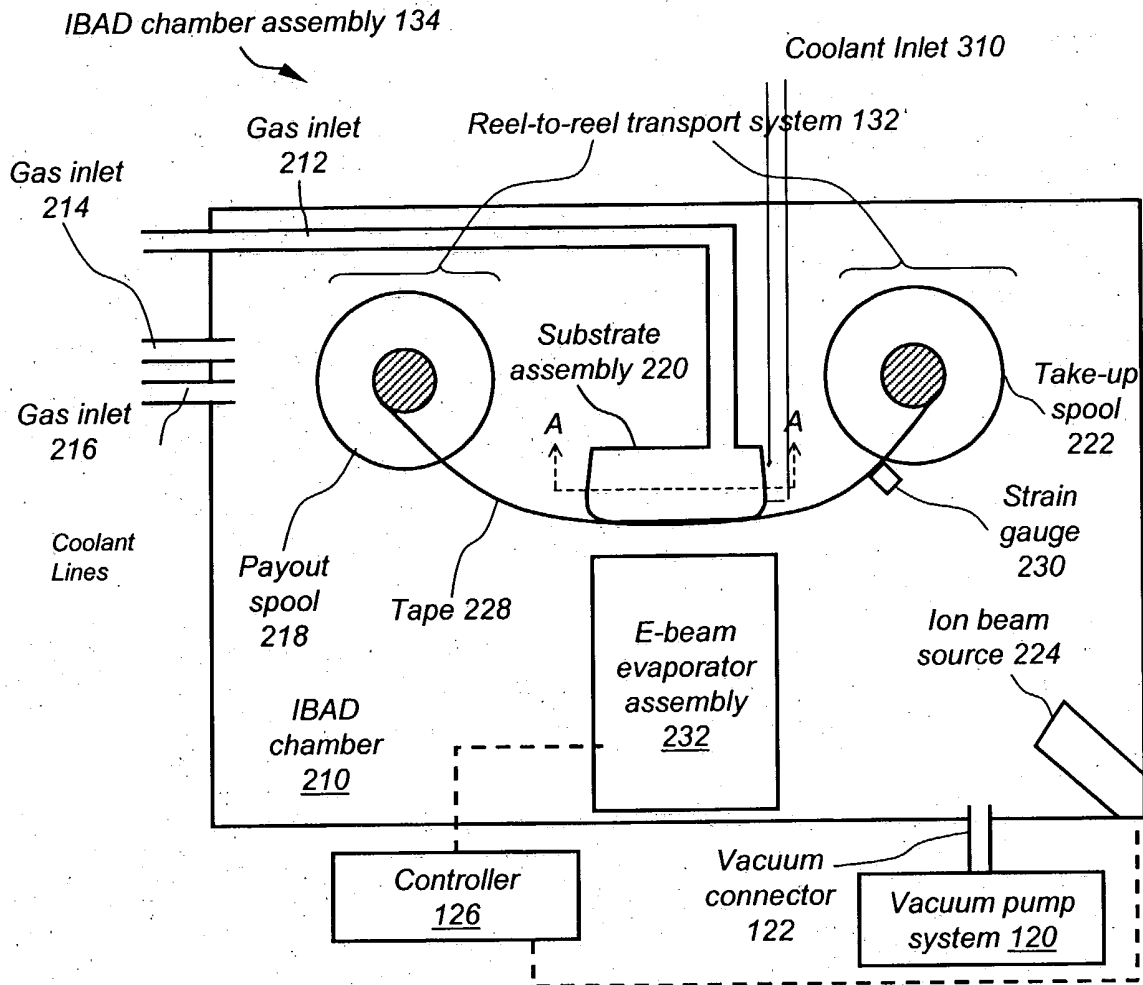


FIG. 2

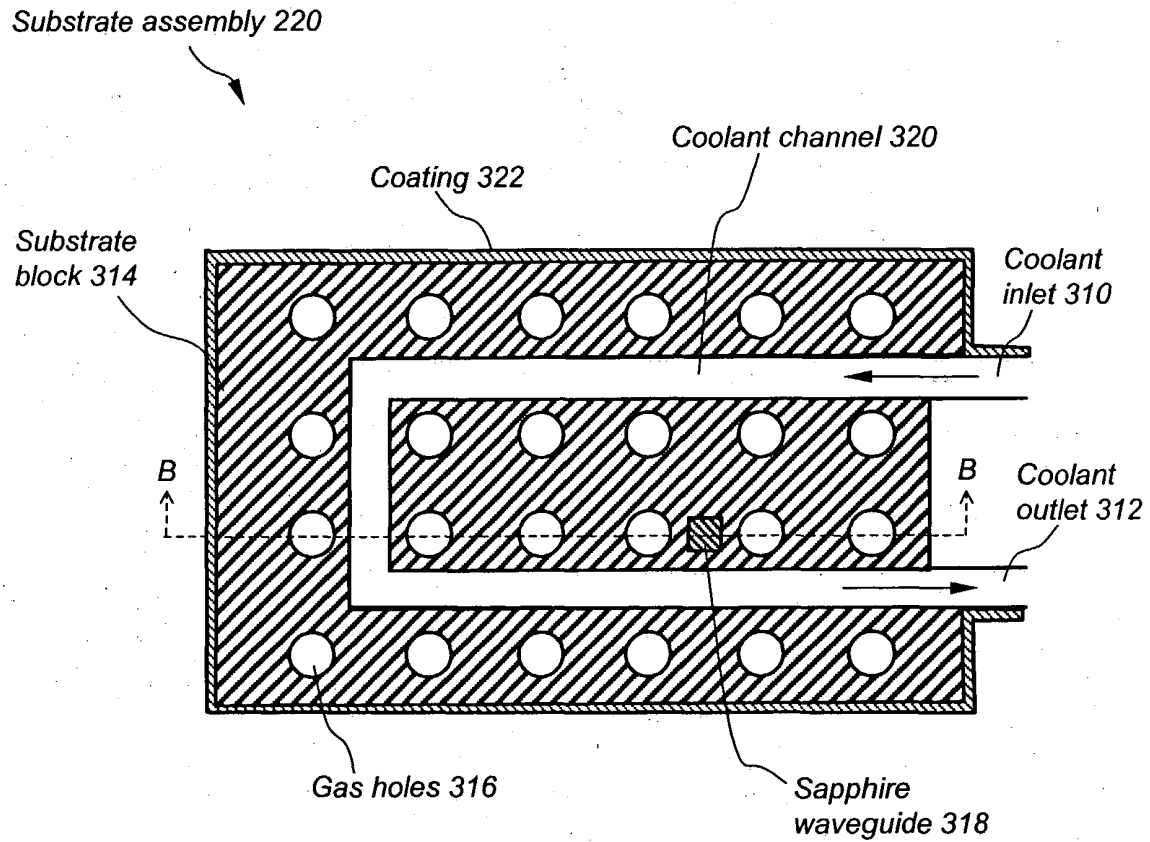


FIG. 3A

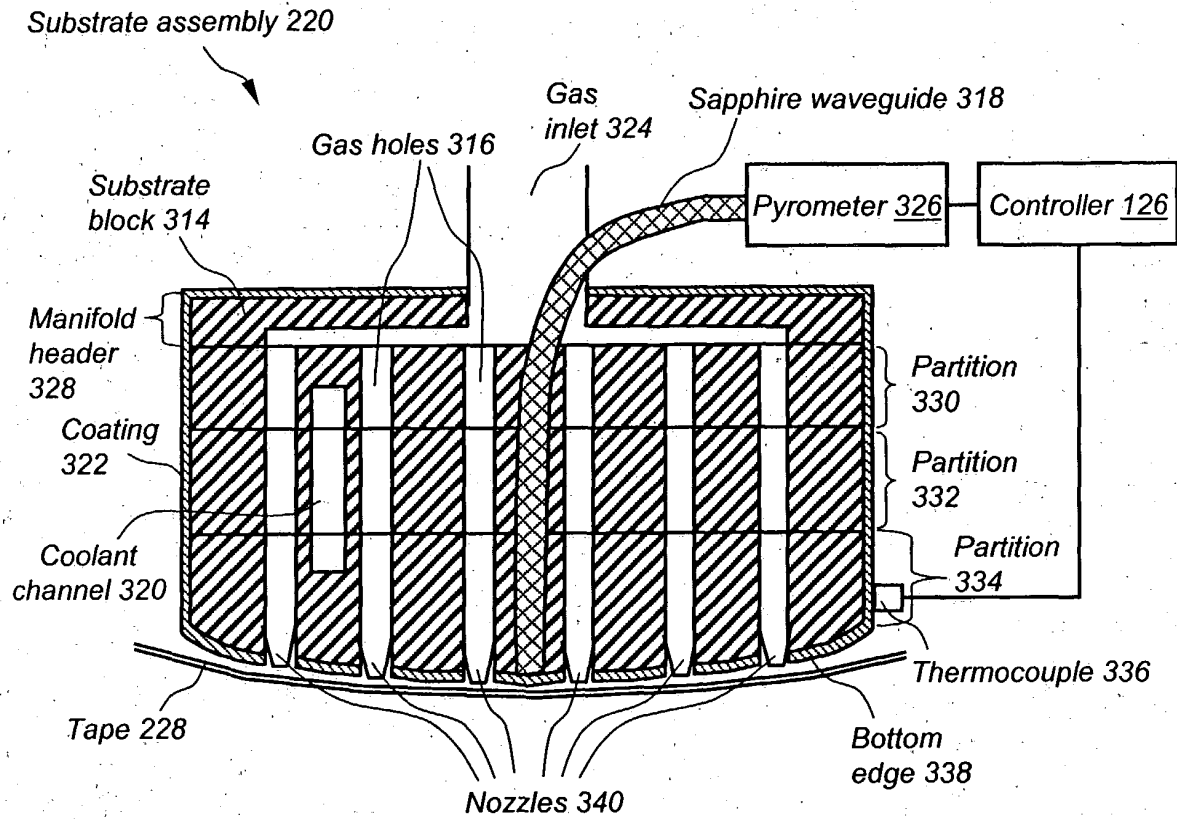


FIG. 3B

Substrate assembly 220

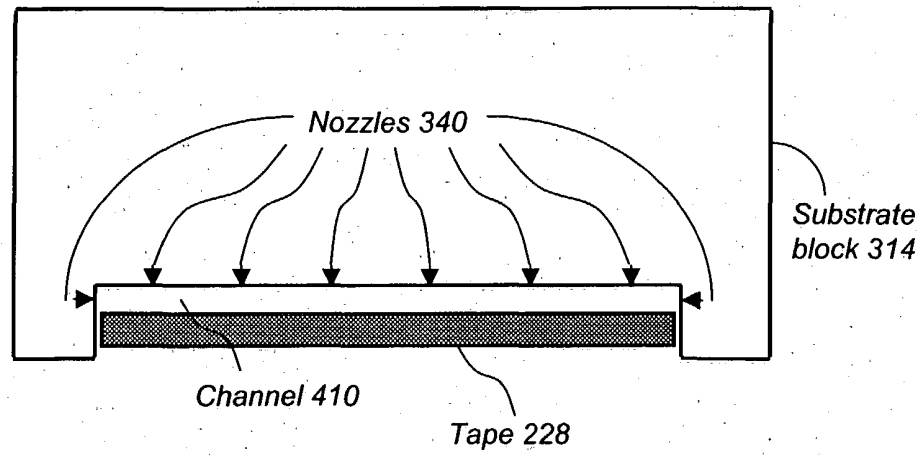


FIG. 4A

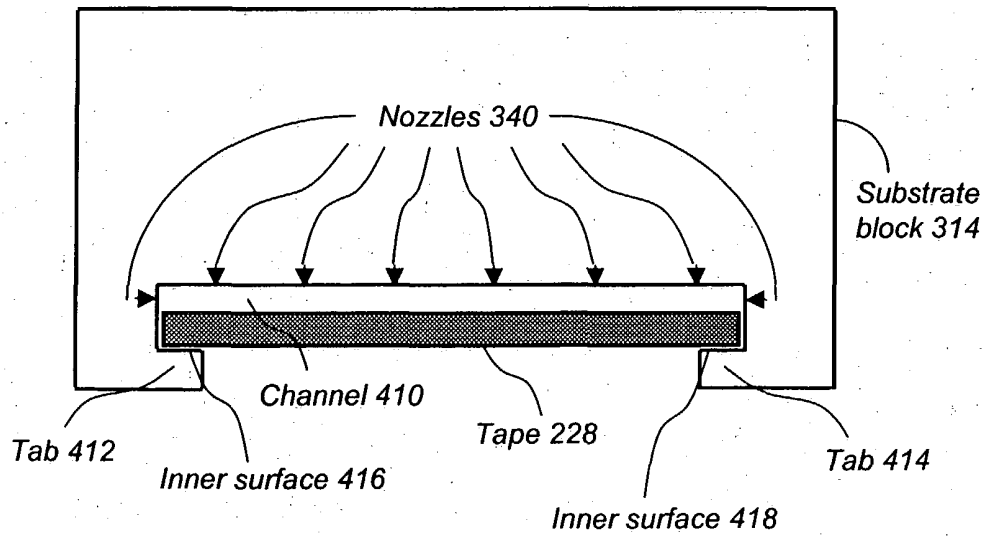


FIG. 4B

Method 500

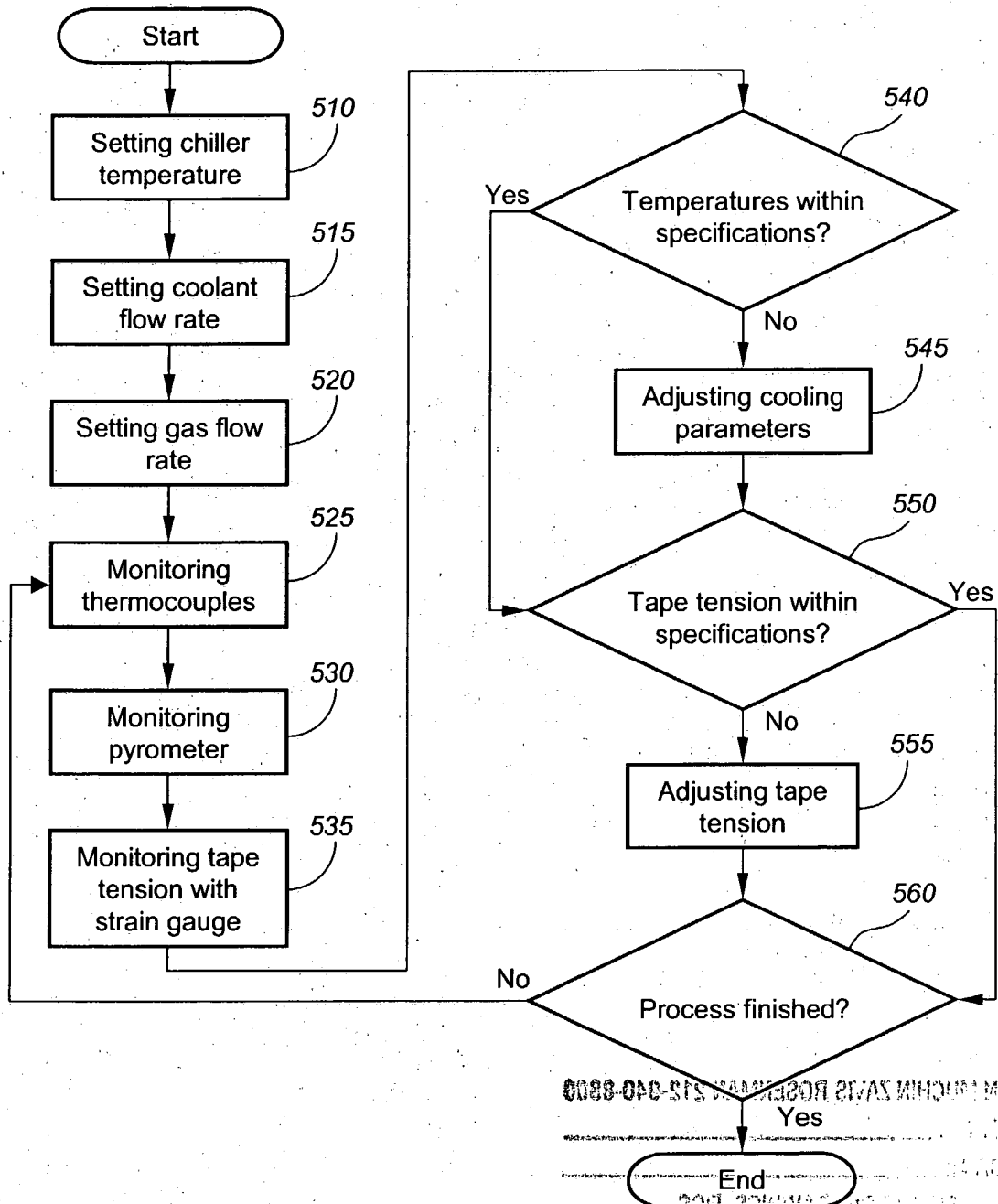


FIG. 5